# Refine Search

## Search Results -

| Terms | Documents |
|---|---|
| L3 and L4 | 14 |

**Database:**

```
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins
```

**Search:**

```
L5
```

[Refine Search]

[Recall Text] [Clear] [Interrupt]

## Search History

**DATE:** **Thursday, December 16, 2004**    Printable Copy    Create Case

| Set Name side by side | Query | Hit Count | Set Name result set |
|---|---|---|---|
| *DB=USPT; PLUR=NO; OP=ADJ* | | | |
| L5 | l3 and L4 | 14 | L5 |
| L4 | (shar$5 or common) near1 memory | 13592 | L4 |
| L3 | l1 and L2 | 54 | L3 |
| L2 | single adj address adj bus | 153 | L2 |
| L1 | processors or (multi-processors) or (multi adj processors) or (multiple adj processors) | 80455 | L1 |

END OF SEARCH HISTORY

# Freeform Search

**Database:**

| US Pre-Grant Publication Full-Text Database |
|---|
| US Patents Full-Text Database |
| US OCR Full-Text Database |
| EPO Abstracts Database |
| JPO Abstracts Database |
| Derwent World Patents Index |
| IBM Technical Disclosure Bulletins |

**Term:** 15 and L6

**Display:** 15 Documents in **Display Format:** CIT,AB **Starting with Number** 1

**Generate:** ○ Hit List ◉ Hit Count ○ Side by Side ○ Image

[Search] [Clear] [Interrupt]

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## Search History

**DATE: Thursday, December 16, 2004**   Printable Copy   Create Case

| Set Name side by side | Query | Hit Count | Set Name result set |
|---|---|---|---|
| *DB=USPT; PLUR=NO; OP=ADJ* | | | |
| L7 | 15 and L6 | 3 | L7 |
| L6 | (6272604 or 5900015 or 5490253).pn. | 3 | L6 |
| L5 | 13 and L4 | 14 | L5 |
| L4 | (shar$5 or common) near1 memory | 13592 | L4 |
| L3 | 11 and L2 | 54 | L3 |
| L2 | single adj address adj bus | 153 | L2 |
| L1 | processors or (multi-processors) or (multi adj processors) or (multiple adj processors) | 80455 | L1 |

END OF SEARCH HISTORY

# Refine Search

## Search Results -

| Terms | Documents |
|---|---|
| L2 and L20 | 17 |

**Database:**

```
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins
```

**Search:**

L21

Refine Search

Recall Text      Clear      Interrupt

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## Search History

**DATE:   Thursday, December 16, 2004**       Printable Copy    Create Case

| Set Name | Query | Hit Count | Set Name |
|---|---|---|---|
| | side by side | | result set |
| | *DB=USPT; PLUR=NO; OP=ADJ* | | |
| L21 | l2 and L20 | 17 | L21 |
| L20 | l17 or l18 or L19 | 120 | L20 |
| L19 | proxy near1 node | 78 | L19 |
| L18 | proxy near1 nodes | 28 | L18 |
| L17 | l15 or L16 | 30 | L17 |
| L16 | proxy near1 processor | 29 | L16 |
| L15 | proxy near1 processors | 1 | L15 |
| L14 | l2 and L13 | 1 | L14 |
| L13 | l1 and L12 | 71 | L13 |
| L12 | single near1 address near1 bus | 166 | L12 |
| L11 | l9 and L10 | 9 | L11 |
| L10 | processors | 80455 | L10 |
| L9 | l6 with through | 15 | L9 |
| L8 | l6 and L7 | 2 | L8 |

| L7 | processors with l4 | 448 | L7 |
|----|----|----|----|

*DB=USPT; PLUR=YES; OP=ADJ*

| L6 | access$4 with l2 with (address near1 bus) | 88 | L6 |
|----|----|----|----|
| L5 | l3 same L4 | 5 | L5 |
| L4 | single near2 bus | 8698 | L4 |
| L3 | l1 same L2 | 268 | L3 |
| L2 | (common or shar$5) near1 memory | 13922 | L2 |
| L1 | master same slave | 19650 | L1 |

END OF SEARCH HISTORY

# Refine Search

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## Search Results -

| Terms | Documents |
|---|---|
| L26 and L29 | 0 |

***

**Database:**

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

**Search:**

L30

Refine Search

Recall Text ⬍    Clear    Interrupt

***

## Search History

**DATE: Thursday, December 16, 2004**    Printable Copy    Create Case

| Set Name side by side | Query | Hit Count | Set Name result set |
|---|---|---|---|
| *DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ* | | | |
| L30 | l26 and L29 | 0 | L30 |
| L29 | single near1 address near1 bus | 213 | L29 |
| L28 | l26 and l12 | 0 | L28 |
| L27 | behalf and L26 | 1 | L27 |
| L26 | l2 same L25 | 50 | L26 |
| L25 | plurality near3 cpus | 1254 | L25 |
| L24 | behalf$2 with L23 | 12 | L24 |
| L23 | proxy near1 (node or processor) | 318 | L23 |
| *DB=USPT; PLUR=NO; OP=ADJ* | | | |
| L22 | l2 and l17 | 3 | L22 |
| L21 | l2 and L20 | 17 | L21 |
| L20 | l17 or l18 or L19 | 120 | L20 |
| L19 | proxy near1 node | 78 | L19 |
| L18 | proxy near1 nodes | 28 | L18 |

| L17 | l15 or L16 | 30 | L17 |
| L16 | proxy near1 processor | 29 | L16 |
| L15 | proxy near1 processors | 1 | L15 |
| L14 | l2 and L13 | 1 | L14 |
| L13 | l1 and L12 | 71 | L13 |
| L12 | single near1 address near1 bus | 166 | L12 |
| L11 | l9 and L10 | 9 | L11 |
| L10 | processors | 80455 | L10 |
| L9 | l6 with through | 15 | L9 |
| L8 | l6 and L7 | 2 | L8 |
| L7 | processors with l4 | 448 | L7 |

*DB=USPT; PLUR=YES; OP=ADJ*

| L6 | access$4 with l2 with (address near1 bus) | 88 | L6 |
| L5 | l3 same L4 | 5 | L5 |
| L4 | single near2 bus | 8698 | L4 |
| L3 | l1 same L2 | 268 | L3 |
| L2 | (common or shar$5) near1 memory | 13922 | L2 |
| L1 | master same slave | 19650 | L1 |

END OF SEARCH HISTORY